Synopsys and Gowin Semiconductor Ink Multi-Year OEM Agreement for FPGA Design Software

Synopsys Synplify Pro Synthesis Tool Delivers Superior Quality of Results for Users of Gowin Semiconductor FPGAs

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Highlights:

- Multi-year agreement provides Gowin FPGA users with Synopsys' Synplify Pro high-quality FPGA synthesis tool to produce high-performance, cost-effective FPGA designs
- Synplify Pro is optimized for Gowin GW2A FPGAs to deliver fast time-to-results with area optimization for cost and power accelerating FPGA development
- Integration with GOWIN design suite provides users with a unified design flow for FPGA implementation

Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced a multi-year OEM agreement with Gowin Semiconductor for Synopsys Synplify Pro® FPGA synthesis tools. The agreement will enable Gowin customers to significantly improve synthesis runtimes and achieve higher quality of results for timing, area and power for Gowin GW2A/3S FPGAs. Gowin Semiconductor has partnered with Synopsys to integrate Synplify Pro into its GOWIN™ design suite for their GW2A/3S FPGAs.

"Our customers require a high performance, high-quality FPGA synthesis flow to help them implement their FPGA designs in hardware, while meeting tight project timelines," said Ning Song, chief technical officer and vice president of FPGA software at Gowin Semiconductor. "The integration of our FPGA software flow with Synplify Pro enables FPGA designers using our GW2A FPGA architecture to achieve the highest quality of results for timing, area and runtime."

Gowin's GW2A/3S FPGAs incorporate highly programmable logic, block SRAM and DSP blocks optimized for performance and power. The GW2A architecture is optimized with Synplify Pro synthesis software, providing designers with the highest performance results with fewer iterations. Designers can take advantage of the wide range of device sizes and I/O capabilities to deliver products for consumer, industrial, communication and computing markets.

"Today's FPGA designs require advanced synthesis tools that deliver automation, faster turnaround times and more predictable timing closure," said John Koeter, vice president of marketing for IP and prototyping at Synopsys. "Synopsys Synplify® FPGA synthesis software is the industry standard for producing high-performance, cost-effective FPGA designs. Integration of Synplify Pro with the GOWIN design suite will help Gowin customers quickly create optimized FPGA implementations that meet precise timing and quality requirements."

Availability & Resources
Synplify Pro for Gowin FPGA devices is available now from Gowin Semiconductor.
Learn more about Synplify Pro: http://www.synopsys.com/synplifypro

About Gowin Semiconductor
Gowin Semiconductor builds field programmable gate arrays (FPGAs) with the goal of creating FPGAs with the best performance and cost ratio. Gowin Semiconductor is a privately held fabless corporation based in Guangdong, China. Learn more at http://www.gowinsemi.com.

About Synopsys
Synopsys, Inc. (Nasdaq: SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, Synopsys delivers software, IP and services to help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at http://www.synopsys.com.

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