Elmos Selects Synopsys' Custom and Digital Design Solutions

Selection of Synopsys' Solutions Based on Superior Results for Analog Mixed-signal Designs

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Highlights of Benefits Realized by Elmos:

- Higher productivity with unified custom and digital co-design implementation solution
- Up to 10X faster mixed-signal verification
- Up to 10 percent smaller area and better silicon correlation

Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced that Elmos has selected Synopsys' comprehensive custom and digital tools to increase the engineering productivity of its mixed-signal design teams. Elmos' decision to switch to Synopsys' solutions is based on the superior results delivered by Synopsys' Galaxy™ Design Platform and mixed-signal verification tools. The tools selected by Elmos include Galaxy Custom Designer® for custom layout, Design Compiler® for synthesis, IC Compiler™ for place and route, PrimeTime® for static timing, StarRC™ for extraction, IC Validator for physical verification, CustomSim™ for circuit simulation and VCS® AMS for mixed-signal verification. The platform delivered up to 10 times faster mixed-signal verification and up to 10 percent smaller silicon area compared to Elmos' existing tools and methodologies. Adoption of Synopsys' solutions will enable Elmos to further extend its leadership in development of mixed-signal semiconductors for the automotive, industrial and consumer markets.

"After extensive evaluations, we concluded our search for a long-term technology partnership for high-performance analog mixed-signal designs with the selection of Synopsys' Galaxy Design Platform," said Jorg Gondermann, vice president of product development at Elmos. "The Galaxy Design Platform provides a unified custom and digital solution that enables us to perform comprehensive, accurate, and up to 10 times faster verification while keeping our schedule with good silicon correlation, all of which are critical for us to meet our customer requirements."

Faster Unified Co-design and Mixed-signal Verification

Synopsys' Galaxy Custom Designer and IC Compiler solutions are seamlessly integrated to provide lossless roundtrip custom editing at any stage of the implementation flow. Design teams can realize faster turn-around-time by easily moving between digital and custom implementation flows while maintaining complete design data integrity. The unified solution accelerates the design development cycle by enabling quick and reliable custom edits at any stage of development, including the time-critical tapeout phase. VCS AMS delivers industry-leading performance and capacity to accelerate regression testing of mixed-signal SoCs up to transistor-level accuracy. VCS-AMS is based on tight integration of VCS functional verification solution and CustomSim FastSPICE simulation engines. It supports complex design architectures mixing SPICE, Verilog, VHDL, SystemVerilog, Verilog-A, and Verilog-AMS.

"We welcome Elmos to our growing list of customers who are pushing the technology limits of mixed-signal designs," said Paul Lo, senior vice president of R&D in the Design Group at Synopsys. "We are committed to helping our customers accelerate their innovation by delivering significant gains in design and verification productivity for advanced mixed-signal designs through innovative EDA solutions."

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, Synopsys delivers software, IP and services to help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at http://www.synopsys.com.

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