Synopsys Announces HAPS Connect Program to Speed Creation of HAPS FPGA-Based Prototypes

New Program Provides Prototypers with Daughter Boards and Services from Synopsys-Approved Third Party Providers

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Highlights:

- HAPS Connect Program enables prototypers to leverage proven solutions from hardware and service vendors for fast prototype bring-up
- Network of approved third party hardware and service vendors provides daughter boards that are optimized for HAPS systems
- The initial partners of the HAPS Connect Program include Back9 Design, eInfochips, Fidus, Gigafirm, hd Lab and Sarokal

Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced the launch of its HAPS® Connect Program to broaden hardware and service offerings for Synopsys HAPS prototyping systems. Back9 Design, eInfochips, Fidus, Gigafirm, hd Lab and Sarokal are the first companies to sign multi-year agreements to provide daughter boards, mechanical design services and protocol testing for Synopsys HAPS systems.

By working with HAPS Connect partners, prototypers can create HAPS-based prototypes faster and easier. The HAPS Connect ecosystem partners have collaborated with Synopsys to deliver:

- Daughter boards that are optimized for HAPS systems including the high-speed proprietary HapsTrak and Multi-Gigabit connectors
- Protocol compliance and testing to validate interface standards and ensure HAPS compatibility
- Mechanical design for custom enclosures for HAPS systems

"With extensive expertise in providing ASIC prototyping services and high performance PCB design, we understand the challenges of developing custom prototypes and creating a right-first-time prototype," said Michael Wakim, chief executive officer at Fidus Systems Inc. "Joining this program gives us the opportunity to collaborate with a leading provider of FPGA-based prototyping systems, and further enhances our ability to deliver customized prototype solutions to our joint customers."

"As a provider of custom interface boards for prototype assembly, it is critical to support the most popular FPGA-based prototype hardware," said Takayuki Yamazaki, chief executive officer at Gigafirm Co., Ltd. "Joining Synopsys' HAPS Connect Program enables us to serve as a reliable hardware vendor for a leading provider of commercial FPGA-based prototyping systems. Our mutual customers can greatly accelerate their time to an operational HAPS prototype by leveraging our design capabilities to tailor solutions for their precise needs."

"As a developer of an innovative Protocol Test System for ASIC and FPGA validation using FPGA-based prototypes, it is essential to provide prototypers with improved testing tools to deliver high-quality prototypes," said Harri Valasma, chief executive officer at Sarokal Test Systems. "We've had a successful long-term relationship with Synopsys and welcome this opportunity to work more closely to help customers accelerate the validation of complex protocols using HAPS prototypes."

The HAPS Connect Program complements Synopsys' internal daughter board development and FPGA-based service offerings. Synopsys service offerings include assistance with prototyping methodology and implementation of reusable prototyping flows that efficiently map ASIC-targeted RTL to HAPS hardware. The design experts at Synopsys can address challenging prototyping issues such as migration of clocks, memories and IP as well as test development and integration.

"The HAPS Connect Program gives customers access to a range of hardware and service providers who have deep knowledge of HAPS systems and expertise in protocol and compliance testing, and in mechanical design," said John Koeter, vice president of marketing for IP and Prototyping at Synopsys. "By working with HAPS Connect Program members and Synopsys' Professional Services, our customers can accelerate time-to-first prototype and quickly deploy prototypes that enable early software development and facilitate system validation. We look forward to working with our partners to make this program a success."

Availability & Resources
For more information on Synopsys' HAPS Connect Program, visit:

https://www.synopsys.com/Systems/FPGABasedPrototyping/Pages/haps-connect-program.aspx.

About Synopsys
Synopsys, Inc. (Nasdaq:SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, Synopsys delivers software, IP and services to help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at https://www.synopsys.com.

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