

# Synopsys Collaborates with Industry Leaders to Publish Book on Advanced Software Development with Virtual Prototypes

Better Software. Faster! Book Shares Best Practices from More Than 10 Contributing Companies across Key Market Segments

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## Highlights:

- Includes case studies from mobile, consumer, industrial and automotive companies that are highly experienced using virtual prototypes for software development and hardware/software integration
- Provides 200 pages of practical guidelines on how virtual prototypes enable concurrent hardware and software development, test and debug up to 12 months before hardware availability
- Written to help developers and managers better understand software development methods that will result in products with higher quality and reliability, and reduce costs

Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced the publication of *Better Software. Faster!*, a practical guide for using virtual prototypes to develop, test and debug software up to 12 months before hardware availability. Virtual prototypes are fast, fully functional software models of systems that execute unmodified production code and provide development, debug and analysis efficiency, which mitigate software development challenges, increase productivity, resulting in better products faster. *Better Software. Faster!* captures the best practices used by a range of companies that have deep experience in deploying virtual prototypes to enable software development and hardware/software integration much earlier in the design cycle and accelerate project schedules.

"Due to the growing size and complexity of software, electronics companies are looking to virtual prototyping solutions to help with the challenge of escalating costs and timelines for embedded software development," said Chris Rommel, vice president, M2M and embedded technology at VDC Research Group. "This book provides a comprehensive overview of the benefits of and best practices to leverage virtual prototypes and stay competitive in an increasingly software-driven market."

"With increasing hardware and software complexity, it is important to have the right software development tools at hand," said Stephan Lauterbach, chief executive officer at Lauterbach. "The *Better Software. Faster!* book describes how companies achieve time-to-market and productivity savings using software debugging tools like our TRACE32 debugger together with virtual prototypes. Software developers will deeply benefit from the many case studies of companies already deploying virtual prototypes to speed their software development schedules."

"This book is a reflection of lessons learned and best practices gained through years of experience and collaboration with our partners and industry leading companies on virtual prototyping," said John Koeter, vice president of marketing for IP and systems at Synopsys. "*Better Software. Faster!* builds on Synopsys' tradition of collaborating with industry experts to educate through instructive publications, enabling companies to achieve and accelerate their development goals to be successful in an extremely competitive environment."

## Availability & Resources

*Better Software. Faster!* is the latest book published by Synopsys Press to help the software developer community achieve earlier software development, debug and test with virtual prototypes. *Better Software. Faster!* is available now.

- - Download a free eBook edition at [www.synopsys.com/vpbook](http://www.synopsys.com/vpbook)
  - View video introducing "Better Software. Faster!" at <https://www.synopsys.com/verification/resources/videos.html>

## **About Synopsys Press**

Synopsys Press offers leading-edge educational publications written by industry experts for the business and technical communities associated with electronic product design. The Technical Series publications provide immediately applicable information on technical topics for electronic system designers, with a special focus on proven industry-best practices to enable the mainstream design community to adopt leading-edge technology and methodology. The publications include the "Verification Methodology Manual for Low Power" (VMM-LP) and the "FPGA-based Prototyping Methodology Manual" (FPMM).

For more information about Synopsys Press, to contribute feedback on any of our publications, or if you have an idea for a potential Synopsys Press book, please navigate to:

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## **About Synopsys**

Synopsys, Inc. (Nasdaq:SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, Synopsys delivers software, IP and services to help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at <http://www.synopsys.com>.

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