

Synopsys Acquires Target Compiler Technologies

Acquisition Expands Synopsys' Portfolio of Software Tools for the Development of ASIPs

MOUNTAIN VIEW, Calif., Feb. 7, 2014 /PRNewswire/ -- Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced it has completed the acquisition of Target Compiler Technologies, a privately held company headquartered in Leuven, Belgium, that provides software tools to design and program application-specific instruction-set processors (ASIPs). ASIPs complement industry-standard processor architectures by enabling designers to implement their own highly specialized software programmable engines for compute-intensive digital signal and data plane processing. The acquisition of Target strengthens Synopsys' existing ASIP tools portfolio while bringing a world-class team of ASIP experts into the company.

"As today's SoCs rely more on heterogeneous multi-core architectures, designers are turning to ASIPs to implement their unique data plane and digital signal processing requirements," said John Koeter, vice president of marketing of IP and Systems at Synopsys. "Target's leading IP Designer and MP Designer software tools perfectly complement Synopsys' offerings for ASIP developers, enabling design teams to develop ASIPs that meet their performance, power, and flexibility requirements more efficiently and with less risk."

Terms of the deal have not been disclosed.

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, Synopsys delivers software, IP and services to help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at www.synopsys.com.

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