

# Synopsys Selected as TSMC's 2013 "Interface IP Partner of the Year" for Fourth Consecutive Year

Award Recognizes Technical Leadership, Number of Customer Tape-Outs, and Outstanding Customer Support

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## Highlights:

- Selection based on customer feedback, TSMC9000 compliance, number of customer tape-outs and technical support excellence
- Synopsys' extensive portfolio of high-quality DesignWare® Interface IP includes USB, PCI Express®, DDR, MIPI®, HDMI, Ethernet and SATA solutions
- Silicon-proven IP available on a wide range of TSMC low-power and high-performance processes from 180nm to 20nm, with a roadmap to 16nm
- DesignWare IP thoroughly characterized across process, voltage and temperature (PVT) variations in both High-K Metal Gate and PolySiON technologies to ensure design robustness

Synopsys, Inc. (Nasdaq:SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced that it received TSMC's 2013 Interface IP Partner of the Year Award for the fourth consecutive year. TSMC has presented Synopsys with this honor every year since TSMC introduced the award in 2010. TSMC selected Synopsys based on customer feedback, TSMC9000 compliance, number of customer tape-outs and technical support excellence. Synopsys' [DesignWare Interface IP](#) portfolio includes widely used protocols such as USB, PCI Express, DDR, MIPI, HDMI, Ethernet and SATA that are available in a broad range of process nodes from 180 nanometer (nm) to 20nm, with a roadmap to 16nm.

"We selected Synopsys due to their continuous commitment to providing our mutual customers with high-quality IP that is TSMC-9000 compliant and because of Synopsys' excellent customer support," said Suk Lee, TSMC senior director, design infrastructure marketing division. "This award recognizes Synopsys' success in delivering silicon-proven IP that helps designers build differentiated products and get to market quickly using TSMC process technology."

"TSMC and Synopsys share a common goal to provide an extensive portfolio of high-performance, proven IP supporting TSMC process technologies, and Synopsys is honored to be recognized for this commitment for the fourth year in a row," said John Koeter, vice president of marketing for IP and systems at Synopsys. "As a trusted IP provider for more than 20 years, we have delivered hundreds of DesignWare IP products on TSMC processes, giving designers access to a broad range of proven IP solutions that enable them to reduce integration risk and accelerate their time to volume production."

## About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven IP solutions for SoC designs. The broad DesignWare IP portfolio includes complete interface IP solutions consisting of controllers, PHY and verification IP for widely used protocols, analog IP, embedded memories, logic libraries, processor cores and subsystems. To support software development and hardware/software integration of the IP, Synopsys offers drivers, transaction-level models, and prototypes for many of its IP products. Synopsys' HAPS® FPGA-Based Prototyping Solution enables validation of the IP and the SoC in the system context. Synopsys' Virtualizer™ virtual prototyping tool set allows developers to start the development of software for the IP or the entire SoC significantly earlier compared to traditional methods. With a robust IP development methodology, extensive investment in quality, IP prototyping, software development and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit <http://www.synopsys.com/designware>.

## About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, Synopsys delivers software, IP and services to help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at [www.synopsys.com](http://www.synopsys.com).

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