OCZ Technology Group Achieves First-Pass Silicon Success with DesignWare IP and Synopsys Professional Services

Silicon-Proven DesignWare IP and Consulting Services Reduce Integration Risk and Help Accelerate Deployment of Complete SoC Design Flow

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Highlights:

- OCZ achieved first-pass silicon success for its NAND flash Vector SSD with Synopsys' DesignWare DDR2/3-Lite PHY, Embedded Memories and STAR Memory System
- Met performance and time-to-market goals with IP integration and physical design assistance from Synopsys' experienced design consultants
- Reduced design iterations and accelerated tape-out schedule using Synopsys' Lynx Design System

Synopsys, Inc. (Nasdaq: SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems, today announced that OCZ Technology Group, Inc. (OCZ) has achieved first-pass silicon success for its NAND flash Vector SSD using Synopsys' DesignWare® DDR2/3-Lite PHY, Embedded Memories, STAR Memory System® solution and Professional Services. Choosing Synopsys technology and services enabled OCZ to accelerate the completion of their project, meet their performance requirements and achieve first-pass silicon success with a production-ready design.

Targeting the high-end consumer market, the OCZ Vector SSD was designed to deliver superior sustained performance through its new, high-performance Indilinx Barefoot 3 flash controller supporting the SATA-3 protocol. For OCZ's design team to effectively focus on developing the Barefoot 3 controller while meeting their broader design goals, they needed reliable, silicon-proven IP, an integrated design flow and design expertise to augment their own. OCZ selected Synopsys' high-performance and area-optimized DDR2/3 PHY, embedded SRAMs and integrated memory test and repair solution. To mitigate risks and accelerate the implementation process, OCZ deployed the Lynx Design System, an integrated RTL-to-GDSII block- and chip-level design environment with unique visualization capabilities for efficient design exploration and reporting of design status and trends. In addition, Synopsys' design consultants worked closely with OCZ's engineers throughout the implementation of their chip, delivering expertise and advanced methodologies in IP integration, physical design, and physical verification that enabled OCZ to complete their implementation in less than six months.

"We were very focused on reducing schedule risk, which made it imperative that we partner with an established vendor like Synopsys that had the proven portfolio of IP, design flows and services that we needed," said Brian McMath, technical director at OCZ. "Of particular value to us was the DDR3 PHY integration expertise Synopsys Professional Services brought to the project. The combination of Synopsys' proven DesignWare IP, experienced design consultants and tapeout-proven Lynx Design System made it possible for us to achieve our performance goals while saving an estimated two months of schedule time. The net result of our collaboration was that we got our product to market much faster than if we had done it on our own."

"Companies such as OCZ who develop innovative electronic products need design solutions they can rely on to mitigate project risks and meet tight time-to-market windows," said John Koeter, vice president of marketing for IP and systems at Synopsys. "Synopsys invests heavily in developing high-quality

DesignWare IP as well as providing tightly correlated tool flows and experienced design consultants so our customers can focus on the aspects of their design that differentiate them from the competition."

Resources

Read the success story: https://www.synopsys.com/dw/doc.php/ss/ocz_ss.pdf

About DesignWare IP

Synopsys is a leading provider of high-quality, silicon-proven IP solutions for SoC designs. The broad DesignWare IP portfolio includes complete interface IP solutions consisting of controllers, PHY and verification IP for widely used protocols, analog IP, embedded memories, logic libraries, processor cores and subsystems. To support software development and hardware/software integration of the IP, Synopsys offers drivers, transaction-level models, and prototypes for many of its IP products. Synopsys' HAPS® FPGA-Based Prototyping Solution enables validation of the IP and the SoC in the system context. Synopsys' Virtualizer virtual prototyping tool set allows developers to start the development of software for the IP or the entire SoC significantly earlier compared to traditional methods. With a robust IP development methodology, extensive investment in quality, IP prototyping, software development and comprehensive technical support, Synopsys enables designers to accelerate time-to-market and reduce integration risk. For more information on DesignWare IP, visit: http://www.synopsys.com/designware.

About OCZ Technology Group, Inc.

Founded in 2002, San Jose, CA-based OCZ Technology Group, Inc. (OCZ) is a global leader in the design, manufacturing, and distribution of high-performance solid-state storage solutions and premium computer components. Offering a complete spectrum of solid-state drives (SSDs), OCZ provides SSDs in a variety of form factors and interfaces (i.e. PCIe, SAS and SATA) to address a wide range of client and enterprise applications. Having developed firmware and controller platforms, to virtualization and endurance extending technologies, the company delivers vertically integrated solutions enabling transformational approaches to how digital data is captured, stored, accessed, analyzed and leveraged by customers. More information is available at www.ocz.com.

About Synopsys

Synopsys, Inc. (Nasdaq:SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, its software, IP and services help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at http://www.synopsys.com.

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